

Prof. Dr. Giovanni Gracioli





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## Personal

- Assistant professor in the [Department of Knowledge Engineering](#) at the [Federal University of Santa](#)

Catarina (UFSC), Florianópolis

- Member of the [Graduate Program in Computer Science](#)
  - Member of the [Graduate Program in Electronics Systems Engineering](#)
  - Member of the [Software/Hardware Integration Lab \(LISHA\)](#)
  - [Weekly Schedule](#)
  - Curriculum vitae ([Lattes](#))
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## PPGESE

- **Aos mestrandos do PPGCC e PPGESE e futuros alunos: está interessado(a) em fazer parte do nosso grupo de pesquisa? Favor encaminhar email.**

\*[Tópicos Sugeridos para Mestrado no PPGCC|Tópicos Sugeridos (mas não limitados) para Mestrado no PPGESE e PPGCC]

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## Research Activities

### Research Topics

- Embedded Operating Systems
  - Features to exploit multicore, reconfigurable, and other emerging architectures
  - Concurrency, inter-processor synchronization, time, and memory management
- Real-time Systems
  - Real-time resource management and scheduling
  - Real-time operating systems (RTOS)

### Projects

- [EPOS: Embedded Parallel Operating System](#) (1997 - )

### Former Research Projects

- [OS support for WSN and Embedded System Debugging](#) (2011 - 2013): CAPES/DFAIT
- [RH-TVD: Special Graduate Program for Digital TV](#)(2009 - 2012): CAPES
- [VAMOS: Variability Management in Operating Systems](#) (2008-2011)
- [IPTV STB: Design and Implementation of a Set-top Box for IPTV](#)(2008 - 2009): FAPESC
- [Rede ALTATV: an Open, Free, Scalable Set-top Box Architecture for DTV and IPTV](#)(2009 - 2010): CTIC

### Publications

<http://www.lisha.ufsc.br/pub/index.php?author=Gracioli>

- External Publications
  - Gracioli, G., and S. Fischmeister, **Tracing Interrupts in Embedded Software**, Proc. of the ACM SIGPLAN/SIGBED Conference on Languages, Compilers, and Tools for Embedded Systems (LCTES), Dublin, Ireland, pp. 137--146, 2009.
  - Gracioli, G., and S. Fischmeister, **Tracing and Recording Interrupts in Embedded Software**, Journal of Systems Architecture, pp. 372-385, 2012.
- Schedulability Results for the SBESC 2020 paper (On the Design and Implementation of Real-Time Resource Access Protocols)
  - [Single-core experiments](#)
  - [Multi-core experiments](#)

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## Teaching (UFSC/Joinville)

- Real-Time Operating Systems
- Data Structures
- Microcontrollers and Digital Systems
- Introduction to Computer Programming
- Embedded Systems
- Embedded Systems Design

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## Professional Activities

### Program Committee Member

- ECRTS2022, ECRTS2021, RTSS2021, SBESC2020, SBESC2019, EWiLi2019, RTAS2019, SBESC2018, SBESC2017, SBESC2016, APRES2015, LCTES2015

### Publication Chair

- [SBESC2015](#)
- [SBESC2016](#)
- [SBESC2017](#)

### Publicity Chair

- [SBESC2020](#)
- [SBESC2021](#)

### Financial Chair

- [SBESC2022](#)

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## Contact

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