



Hugo Marcondes (M.Sc.)

Profession

- Project Manager and Researcher at the [Laboratory for Software and Hardware Integration \(LISHA\)](#), of the [Federal University of Santa Catarina \(UFSC\)](#)
 - [Weekly schedule](#)
 - [Curriculum Vitae at CNPq \(Lattes\)](#)
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Teaching Activities

Programs

- [Undergraduate in Computer Science](#)

Former Lectures

- [Operating System 2008.2](#) (Assistent)
 - [System Programming 2006.2](#) (Assistent)
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Research Activities

Research Topics

- **Embedded Systems Development**
- **Multimedia Systems**
- **System-level Design**
- **Hardware and Software Co-Design**
- **Data Storage Systems**

Current Projects

- [vConf - Corporate Video Conferencing](#) (2010 -): Intelbras
- [EPOS: Embedded Parallel Operating System](#) (1997 -)

Former Projects

- [REDE ALTATV - Arquitetura Livre, Aberta e Escalável de Terminal de Acesso para o SBTVD-T](#) (2009 - 2010): RNP/MCT
- [Codificador H.264 para Televisão Digital](#) (2008 - 2010): FINEP
- [Set-top box IPTV](#) (2008 - 2009): FAPESC
- [PMM: Plataforma Multimídia Modular](#) (2006 - 2008): FINEP
- [SBTVD-MIDIASUL - Camada de Transporte do Sistema Brasileiro de Televisão Digital](#) (2005 - 2005): FINEP
- [PDSCE: Embedded System Development Platform](#) (2005 - 2007): FINEP

Contact

UFSC/CTC/LISHA ([map](#))

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