Ms. João Paulo Bedretchuk

Education

- Ph.D. candidate Electrical Engeneering, at the Federal University of Santa Catarina (UFSC).
- M.Sc in Electronic Systems Engineering from the Federal University of Santa Catarina (UFSC).
- B.Eng. in Electrical Engineering from the Contestado University (UnC)
- Curriculum Vitae at CNPq (Lattes)

Research Activities

Research Topics

- Embedded Systems Development;
- Micro-Electro-Mechanical Systems (MEMS)
- MEMS Accelerometers
- Seismic data acquisition

Projects

• Intelligent Acquisition and Analysis System for ECUs (IASE)

Contact

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