


João Paulo Bedretchuk

Education

- M.Sc. student in Electronic Systems Engineering at [Federal University of Santa Catarina \(UFSC\)](#).
- B.Eng. in Electrical Engineering from the [Contestado University \(UnC\)](#)
- [Curriculum Vitae at CNPq \(Lattes\)](#) 

Research Topics

- Embedded Systems Development;

Personal Interests

- Embedded Systems

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