

Marco Aurélio Wehrmeister (Ph.D.)

Profession

- Researcher at the [Software/Hardware Integration Lab \(LISHA\)](#), of the [Federal University of Santa Catarina \(UFSC\)](#)
- Professor at [Department of Computer Science](#), at [Santa Catarina State University \(UDESC\)](#)

Links

- [Weekly schedule](#)
- [Personal website](#)
- [Curriculum Vitae at CNPq \(Lattes\)](#)

Research Topics

- Distributed and Embedded Real-Time Systems (DERTS)
- Model-Driven Engineering (MDE)
- High-Level Modeling (UML, MARTE profile)
- Aspect Oriented Design
- Code generation
- Aspects Weaving
- UML model simulation/execution
- Hardware/Software co-Design
- Synthesis of hardware/software components of DERTS

Current Projects

Former Projects

Publications

Contact

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