

Murilo Ferreira Vitor

Profession

- B. Sc. Student in [Automation and Control Engineering](#) at [Federal University of Santa Catarina \(UFSC\)](#) (7th period)
 - Researcher of the [Software/Hardware Integration Lab \(LISHA\)](#) at UFSC
 - [Weekly schedule](#)
 - [Curriculum Vitae at CNPq \(Lattes\)](#)
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Research Activities

- **Real-time operating systems (RTOS)**
 - Scheduling for real-time multicore platforms

Current Projects/Experiments

- [Multicore Central for Recognizing Voice Commands](#)

Former Projects

- [Implementation and analysis of Real Time Schedulers for multicore processors \(2012-2013\)](#)
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Contact

UFSC/CTC/LISHA ([map](#))

PO Box 476

88049-900 Florianópolis - SC - BRAZIL

Phone/Fax: +55 48 3721-9516

E-mail: [murilo at lisha.ufsc.br](mailto:murilo@lisha.ufsc.br)