

João Gabriel Reis



Personal

- M.Sc. student in Electrical Engineering at [Federal University of Santa Catarina \(UFSC\)](#) holding a [CAPES](#) scholarship
 - Electronics Engineer graduated at [National Polytechnic Institute of Toulouse \(INPT\) - ENSEEIHT](#)
 - Electrical Engineer graduated at [Federal University of Santa Catarina \(UFSC\)](#)
 - [Curriculum Vitae](#)
 - [Curriculum Vitae at CNPq \(Lattes\)](#)
 - [Weekly schedule at LISHA](#)
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Publications

- [BibTeX](#)
- [Full list](#)

Teaching Activities

Lectures

- Teaching assistant in [Embedded Systems Laboratory](#) 2014/2

Advising

- I can advise undergraduate students in themes related to my research topics. Check the [information for applicants](#) to see how to write your monograph under my supervision.
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Research Activities

Research Topics

- Dynamically Reconfigurable Embedded Systems
 - Operating system support for FPGA dynamic partial reconfiguration
 - Handling heterogeneity and variability in hardware through reconfiguration
- Cyber-Physical Systems
 - Mapping Models of Computations (MoC) into RTOS abstractions

Former Projects

- [CIA2: Building SmartCities](#) (2011 - 2013): CTIC/RNP

Projects

- [IPSense](#) (2014 -)
- [SmartHome](#) (2009 -)
- [EPOS: Embedded Parallel Operating System](#)
- [EPOSMote](#)

Professional Activities

- ASIC Engineer Intern at Lyberta, Toulouse, France, June 2012–September 2012
- Embedded Systems Engineer Intern at [Parrot SA](#), Paris, France, February 2013–August 2013.

Contact

UFSC/CTC/LISHA ([map](#))

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Other

- [GitHub](#)
- [Bitbucket](#)