Murilo Ferreira Vitor

Profession

- B. Sc. Student in Automation and Control Engineering at Federal University of Santa Catarina (UFSC) (7th period)
- Researcher of the Software/Hardware Integration Lab (LISHA) at UFSC
- Weekly schedule
- Curriculum Vitae at CNPq (Lattes)

Research Activities

- **Real-time operating systems (RTOS)**
  - Scheduling for real-time multicore platforms

Current Projects/Experiments

- Multicore Central for Recognizing Voice Commands

Former Projects

- Implementation and analysis of Real Time Schedulers for multicore processors (2012-2013)

Contact

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