

Rodrigo Vieira Steiner

Profession

- B.Sc in [Computer Science](#) from the [Federal University of Santa Catarina \(UFSC\)](#)
 - M.SC Student in [Computer Science](#) at [Federal University of Santa Catarina \(UFSC\)](#)
 - Researcher of the Software/Hardware Integration Lab (LISHA) at UFSC
 - [Weekly schedule](#)
 - Curriculum vitae([Lattes](#))
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Research Activities

Research Topics

- Embedded Systems
- Operating Systems
- Wireless Sensor Networks

Projects

- EPOS: Embedded Parallel Operating System (1997 -)
- Rede ALTATV: an Open, Free, Scalable Set-top Box Architecture for DTV and IPTV (2009 - 2010): CTIC

Former Research Projects

- EPOS Mote
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Contact

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